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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Horatio Lo, Sam Tam, David Lee, Dietmae M. Kurpanek

Assignee:

Vicom Systems, Inc.

Title:

Distributed Storage Management Platform Architecture

Serial No.:

09/904,824

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Date of Patent:

April 5, 2005

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Examiner:

Christopher B. Shin

Group Art Unit:

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of Correction

Docket No.:

VCM0002P1US

Austin, Texas August 31, 2005

ATTN: CERTIFICATE OF CORRECTION BRANCH

OF THE PATENT ISSUE DIVISION

COMMISSIONER FOR PATENTS

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REQUEST FOR ENTRY OF CERTIFICATE OF CORRECTION

Sir:

Please enter the enclosed Certificates of Correction (PTO Form 1050) in the above patent. The errors sought to be corrected were made by

the Patent and Trademark Office. Thus, no fee is required for the Certificate of Correction pursuant to 37 CFR §1.322.

Applicant(s) (at least in part). See next section for explanation. The appropriate fee under 37 CFR §1.323 has been authorized below.

Please correct the errors, as noted in the enclosed Certificates of Correction, made by the USPTO. All inquiries concerning this request should be directed to the undersigned attorney at (512) 439-5084.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Attn: Certificate Of Correction Branch Of The Patent Issue Division, Commissioner For Patents, P. O. Box 1450, Alexandria, V.A. 223/3-1450, on August 31, 2005.

Attorney for America

Date of Signatu

Respectfully submitted,

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.

6,877,044 B2

ISSUE DATE

April 5, 2005

INVENTOR(S)

Horatio Lo, Sam Tam, David Lee, Dietmar M. Kurpanek

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At Column 61, line 45, Claim 1 should read as follows:

--1. An apparatus comprising:

a storage virtualization engine (SVE), wherein

said SVE comprises a plurality of multi-port controllers,

at least one of said multi-port controllers comprises a plurality of ports and an interface controller,

said interface controller is associated with one of said ports,

said at least one of said multi-port controllers is configured to

determine a state of said interface controller, wherein said interface controller is

configured to receive a signal, and

enable said port if said signal is valid .--

At Column 61, line 47, Claim 2 should read as follows:

--2. The apparatus of claim 1, wherein

said at least one of said multi-port controllers is configured to configure said one of said ports of said at least one of said multi-port controllers.--

Claim 3 should be dependent on Claim 2 not Claim 1, therefore: At column 61, line 49, please delete "claim 1" and insert --claim 2--

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UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

6,877,044 B2

ISSUE DATE

April 5, 2005

INVENTOR(S)

Horatio Lo, Sam Tam, David Lee, Dietmar M. Kurpanek

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At Column 61, line 52, Claim 4 should read as follows:

--4. The apparatus of claim 2, wherein

said at least one of said multi-port controllers is configured to communicatively couple said SVE to another SVE .--

At Column 61, line 59, Claim 6 should read as follows:

--6. The apparatus of claim 2, further comprising:

a storage subsystem, wherein

a multi-port controller of said multi-port controllers is coupled to said storage subsystem.--

At Column 61, line 63, Claim 7 should read as follows:

The apparatus of claim 2, further comprising:

a host computer, wherein

a multi-port controller of said multi-port controllers is coupled to said host computer .--

Claim 9 should be dependent on Claim 8 not Claim 1, therefore: At column 62, line 4, please delete "claim 1" and insert --claim 8--

At Column 62, line 11, Claim 10 should read as follows:

--10. The apparatus of claim 8, wherein

a multi-port controller of said multi-port controllers of said SVE is communicatively coupled to a multiport controller of one of said SVEs .--

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